

CO DESIGNING A HIGH PERFORMANCE AND PORTABLE LIBRARY (QMCKL): ONE OF THE MAJOR CHALLENGES ADDRESSED BY TREX COE

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### TREX in a nutshell

# TREX Mission: To develop, promote, and maintain open-source, exascale-ready software solutions in (stochastic) quantum chemistry

Materials modeling at the nanoscale with extreme accuracy



- × Scientists in quantum chemistry, physics, and machine learning
  - × Software and HPC experts
- × Tech and communication SMEs
- × Representative of user communities





#### What: Use Quantum Monte Carlo Methods

- > Highly accurate
- Massively parallelizable (multiple QMC trajectories)
- No Blocking communications
- CPU intensive (difficult to exploit)

**Objective: Make codes ready for exascale** 

How: Instead of re-writing codes, provide libraries

> A library for exchanging information between codes:

TREXIO => Enables HTC

A library for high-performance numerical computation:
QMCkl = Enables HPC

Create a platform of interoperable flagship codes extendable and/or operable with codes outside TREX



- 1. **PRODUCTIVITY**: Used and developed by scientists, can be called from different languages
- 2. **PORTABILITY:** available on large number of hardware and software platforms
- 3. **PERFORMANCE:** Must be efficient

"Classical" challenge : be good simultaneously on the 3 objectives. There is a workshop at SC devoted to this triple objective.

We need to go beyond these classical objectives:

- > Not only performance but also energy consumption should be a major goal
- Numerical accuracy is extremely important



# Major axes/guidelines

- Focus and Specialization: focused on well identified objectives (not general)
- Ease of interaction with software and hardware environment: beyond classic portability
- 3. Use of Advanced Tuning Tools: adaptable library via tools instead of static library



- Application target = QMC: use of high degree of parallelism present in most QMC applications. Focus on single node/core implementations
- 2. Focus on a few reference platforms: CPU (ISA: X86 + ARM Neoverse), GPU
- **3. Specific needs of our target applications:** for example no need of general arbitrary size Matrix Multiplies. Many of our apps are using Rank K Updates: (MxK) x (KxM) with K much smaller than M

#### **GOOD NEWS:**

- For our propose (scientific computing) X86 and ARM general architecture share a lot of common characteristics with secondary differences which can be dealt with automatically
- CPU and GPU all strongly require vectors.... But memory constraints are very different



- 1. Interaction with context: routines available in source form so compiler can inline and optimize through calls
- 2. Develop optimized but generic code versions: use tools for generating highly optimized and specific version
- 3. Strongly structure arrays within the library: systematic use of tiled arrays for improving memory hierarchy usage
- 4. Systematic use of (1+n) library versions: a pedagogical/reference version and several optimized versions
- OPEN SOURCE + STANDARDS: easy to modify and integrate. Strong use of standard: OpenMP directives for GPU and vectorization



- 1. Obtain high performance across a large range of platforms (first CPU)
- 2. **Provide input for compilers/library/hardware designers**

#### Approach

- Start from the generic version perform first level (generic) optimization following tools guidance
- Perform detailed analysis of hardware and software interaction (including low level)
- Use tools (MAQAO) to automate info gathering and performance comparison
- Use tools (MAQAO) for generating specialized versions in partocular auto tuners for last mile optimization



# Key issues analyzed

- 1. **Profile categorizations:** time spent in libraries, binary, loops (innermost/outermost), etc...
- 2. Flow complexity: number of paths, presence of calls, etc...
- 3. Array access: unit/non unit stride access, indirect
- 4. Vectorization: not only amount of vector instruictions but also assess vectorization quality

#### **IMPORTANT:**

- All of the above analysis is performed at the ASM level (either statically or dynamically at run time)
- This analysis depends upon compiler and processor used

GOOD NEWS: we can test various compilers and hardware and perform comparative studies. Very usefull for vectorization.



### Provide performance estimates when specific optimizations are triggered

- 1. Perfect OpenMP/MPI/Pthread: suppress time spent in these parallelism libraries
- 2. Perfect OpenMP/MPI/Pthread + Perfect Load balancing: suppress time spent in these parallelism libraries + perform perfect load balancing
- 3. **Perfect compiler**: gets rid of all of the "integer" operations
- 4. Perfect arithmetic FP vectorization: assumes arithmetic FP vectorization
- 5. **Perfect Full vectorization**: assumes arithmetic FP + Load/Store vectorization
- 6. L1 data access: assumes that all data access are performed from L1

**IMPORTANT:** all of these performance estimates are computed at the loop level but their performance impact is extrapolated at the whole application.



## Target: Unicore Skylake Xeon(R) Platinum 8170 + ICC/IFORT 2021 – O3

► Colums Filter														
Loop id	Source Location	Source Function	Level	Coverage run_0 (%)	Vectorization Ratio (%)	Vectorization Efficiency (%)	Speedup If No Scalar Integer	Speedup If FP Vectorized	Speedup If Fully Vectorized	Speedup If Perfect Load Balancing run_0	Stride 0	Stride 1	Stride n	Stride Unknown
703	libqmckl.so.0 - q mckl_jastrow_f.F 90:2088-2105 []	qmckl_compute_f actor_een_deriv_e _f	Innermost	12.56	100	50	1.3	1.21	2	1	1	12	0	1
<mark>602</mark>	libqmckl.so.0 - q mckl_jastrow_f.F 90:798-815	qmckl_compute_f actor_een_rescale d_e_deriv_e_f	Innermost	7.74	55.56	30.56	1	1	3.55	1	0	6	0	0
<mark>64</mark> 0	libqmckl.so.0 - q mckl_jastrow_f.F 90:1050-1067	qmckl_compute_f actor_een_rescale d_n_deriv_e_f	Innermost	2.15	56.76	31.08	1	1	3.42	1	0	6	0	0
155	libqmckl.so.0 - q mckl_jastrow.c:1 649-1653	qmckl_compute_e en_rescaled_e_hp c	Innermost	1.98	0	12.5	1	1	8	1	0	2	0	2
<mark>603</mark>	libqmckl.so.0 - q mckl_jastrow_f.F 90:798-815	qmckl_compute_f actor_een_rescale d_e_deriv_e_f	Innermost	1.29	0	12.5	1	1	8	1	0	2	0	4
<mark>606</mark>	libqmckl.so.0 - q mckl_jastrow_f.F 90:784-789	qmckl_compute_f actor_een_rescale d_e_deriv_e_f	Innermost	1.17	100	44.74	1.02	1.35	2.46	1	0	5	0	0
161	libqmckl.so.0 - q mckl_jastrow.c:1 630-1633	qmckl_compute_e en_rescaled_e_hp c	Innermost	0.39	0	12.5	1.2	1.5	8	1	0	0	0	0



### Target: Unicore Skylake Xeon(R) Platinum 8170 + ICC/IFORT 2021 – O3

Global Metrics		?
Total Time (s)	<b>54.8</b> 3	
Profiled Time (s)	52.76	
Time in analyzed loops (%)	30.9	
Time in analyzed innermost l	29.8	
Time in user code (%)	31.3	
Compilation Options	OK	
Perfect Flow Complexity	1.00	
Iterations Count	1.00	
Array Access Efficiency (%)	92.8	
Perfect OpenMP + MPI + Pthre	1.00	
Perfect OpenMP + MPI + Pthre	1.00	
No Scalar Integor	Potential Speedup	1.04
No Scalal Integer	Nb Loops to get 80%	2
EP Vactorised	Potential Speedup	1.03
FF vectorised	Nb Loops to get 80%	3
Fully Vectorised	Potential Speedup	1.25
Fully vectorised	Nb Loops to get 80%	6
Data In L1 Cacho	Potential Speedup	1.10
	Nb Loops to get 80%	1
EP Arithmatic Only	Potential Speedup	1.16
TT And medic Only	Nb Loops to get 80%	7



MAQAO ANLYSIS OF JASTROW ROUTINE

### Target: Unicore Skylake Xeon(R) Platinum 8170 + ICC/IFORT 2021 – 03

□ CQA speedup if no scalar integer □ CQA speedup if FP arith vectorized □ CQA speedup if fully vectorized Number of paths Analysis □ Vectorization Ratio (%) □ Vectorization Efficiency (%) 🗹 ORIG / DL1  $\Box$  Saturation ratio (MAX(DL1,LS)/REF) FP/CQA(FP) DL1/CQA(DL1) FP/LS Saturation ORIG (cycles per iteration) STA (ORIG) ✓ REF (cycles per iteration)
✓ STA (REF) FP (cycles per iteration) 🗹 STA (FP)  $\checkmark$  LS (cycles per iteration) STA (LS) DL1 (cycles per iteration) 🗹 STA (DL1) FES (cycles per iteration) 🗹 STA (FES) CQA cycles ✓ CQA cycles if no scalar integer CQA cycles if FP arith vectorized CQA cycles if fully vectorized Iteration count Function Source Nb FP\_ADD / CPI CAP(FP) Nb FP\_MUL / CPI SAT(L2) BW(FP) SAT(FP) CAP(L1R) BW(L1R) SAT(L1R) CAP(L1W) 🗹 BW(L1W) SAT(L1W)  $\checkmark$  CAP(L2) BW(L2)  $\checkmark$  CAP(L3) BW(L3) SAT(L3)  $\checkmark$  CAP(RAM\_R)  $\checkmark$  CAP(RAM\_W) Select none

ID	Module	Coverage (% app. time)	Analysis	Number of paths	ORIG / DL1	Saturation	FP/CQA(FP)	DL1/CQA(DL1)	FP/LS	ORIG (cycles per iteration)	STA (ORIG)	REF (cycles per iteration)	STA (REF)	FP (cycles per iteration)	STA (FP)	LS (cycles per iteration)	STA (LS)	DL1 (cycles per iteration)
▼ Loop 703 +	libqmckl.so.0	12.56	RAM bound	1	3.81	SATURATED	1.21	1.36	0.16	95.74	0.75	114.41	0.48	17.54	0.04	112.36	0.46	25.13
• Bucket 7		66.99	RAM bound	1	3.81	SATURATED	1.21	1.36	0.16	95.74	0.75	114.41	0.48	17.54	0.04	112.36	0.46	25.13
∘ Bucket 6		18.57	RAM bound	1	2.75	UNSATURATED	1.19	1.36	0.28	69.18	0.55	72.46	0.48	17.28	0.09	61.54	0.51	25.13
∘ Bucket 8		13.42	RAM bound	1	6.12	UNSATURATED	1.20	1.36	0.13	154.00	0.36	143.49	0.79	17.44	0.10	134.72	0.35	25.18



## QMCkl: a QMC driven library

- 1. Oriented towards performance/portability/productivity but with customized objectives
- 2. Focused and specialized
- 3. Easy to interact with
- 4. Strongly focused on co design
- 5. Using systematically software tools: multiple versions depending upon target architectures

#### **STATUS:**

- A first X86 version available
- GPU and ARM first versions available within 12 to 18 months





- >TREX web site: <u>https://trex-coe.eu</u>
- >TREXIO: <u>https://github.com/trex-coe/trexio</u>
- >QMCkl: <u>https://github.com/trex-coe/qmckl</u>
- QMCkl documentation: <u>https://trex-coe.github.io/qmckl</u>
- MAQAO: <u>http://www.maqao.org</u>
- Verificarlo: <u>https://github.com/verificarlo/verificarlo</u>





Preserve **numerical accuracy** for new architectures, parallel runtimes, optimizations. **Verificarlo** is a tool for assessing the precision of floating point computations.



github.com/verificarlo/verificarlo GPL v3

- Find numerical bugs in codes [1]
  - Stochastic arithmetic to simulate round-off and cancellations
  - Localization techniques to pinpoint source of errors
  - Track precision through CI framework
- Optimize precision [2]
  - Simulate custom formats for mixed precision (float, bf16)
  - Tune precision in math library calls

[1] Numerical uncertainty in analytical pipelines leads to impact ul variability in brain networks. Kiar et al. 2021 PLOS ONE.
 [2] Study of the effects and benefits of Custom-Precision Mathematical libraries in HPC codes. Brun et al. 2021 IEEE TETC.



### Numerical accuracy: some applications

/ YALES2). [3]

Harness mixed-precision: 30% speed-up

in deflated conjugate gradient (560 cores

Track kernel accuracy during the development process of QMCkl.



Git commits (QMCkl – SMWB kernels)

[3] Automatic exploration of reduced floating-point representations in iterative methods. Chatelain et al. Europar'19.



- 1. Use of automatic generation tools: from a high level generic ASM generate X86 and ARM versions. This will allow to directly embed low level code ("ASM volatile").
- 2. Use of autotuning tools: very useful for last mile optimization, explore automatically different code variants and parameters.
- 3. Use of advanced performance analysis tools: monitor not only vectorization ration (% of vector instructions) but also vectorization efficiency (vector width used).
- 4. Use of numerical accuracy monitoring tools: in particular identify code fragments sensitive to accuracy.



A few hundreds of source code lines but restructured for heavy use of dense matrix multiplication operations

The initial equation implemented in CHAMP is:

$$J_{\text{een}}(\mathbf{r}, \mathbf{R}) = \sum_{\alpha=1}^{N_{\text{nucl}}} \sum_{i=1}^{N_{\text{elec}}} \sum_{j=1}^{i-1} \sum_{p=2}^{N_{\text{nord}}} \sum_{k=0}^{p-1} \sum_{l=0}^{p-k-2\delta_{k,0}} c_{lkp\alpha} (r_{ij})^k \left[ (R_{i\alpha})^l + (R_{j\alpha})^l \right] (R_{i\alpha} R_{j\alpha})^{(p-k-l)/2}$$

It was rewritten as

$$J_{\text{een}}(\mathbf{r}, \mathbf{R}) = \sum_{p=2}^{N_{\text{nord}}} \sum_{k=0}^{p-1} \sum_{l=0}^{p-k-2\delta_{k,0}} \sum_{\alpha=1}^{N_{\text{nucl}}} c_{lkp\alpha} \sum_{i=1}^{N_{\text{elec}}} \bar{\mathbf{R}}_{i,\alpha,(p-k-l)/2} \bar{\mathbf{P}}_{i,\alpha,k,(p-k+l)/2}$$

with

$$\bar{\mathsf{P}}_{i,\alpha,k,l} = \sum_{j=1}^{N_{\mathsf{elec}}} \bar{\mathtt{r}}_{i,j,k} \ \bar{\mathtt{R}}_{j,\alpha,l}.$$